

# Model 585 Clipped Sine Wave TCXO/VC-TCXO

#### **Features**

- Ceramic Surface Mount Package
- Fundamental Crystal Design
- Frequency Range 5 52MHz \*
- Operating Voltage +2.5V, +3.0V and +3.3V
- Frequency Stability, Options from ±0.5ppm to ±2.5ppm
- Operating Temperature Range to -40°C to +85°C
- Voltage Control Option for Frequency Tuning [VCTCXO]
- Tape and Reel Packaging, EIA-481

# **Applications**

Base Stations

- WLAN/WiMAX/WiFi
- Synchronous Ethernet
- Femtocells and Microcells

# Part Dimensions:

#### Standard Frequencies

\* See Page 8 for common frequencies. Check with factory for frequency availability.

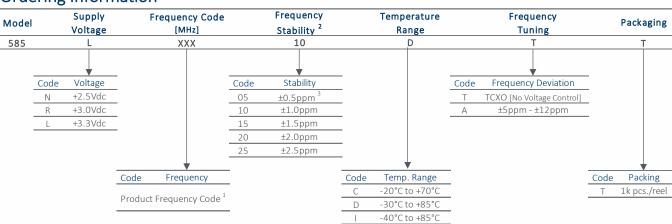
7.0 × 5.0 × 2.1mm • 177mg

- Wireless Communication
- Test and Measurement

# Description

CTS Model 585 is a high performance Temperature Compensated Crystal Oscillator [TCXO]. Employing analog IC technology that provides a clipped sine wave output, high-order temperature compensation engine; coupled with a fundamental quartz crystal M585 has excellent stability and low jitter/phase noise performance.

# **Ordering Information**



#### Notes:

- 1] Refer to document 016-1454-0, Frequency Code Tables. 3-digits for frequencies <100MHz, 4-digits for frequencies 100MHz or greater.
- 2] Frequency vs. Temperature only.
- 3] Check factory for availability with "I" temperature range.

Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

This product is specified for use only in standard commercial applications. Supplier disclaims all express and implied warranties and liability in connection with any use of this product in any non-commercial applications or in any application that may expose the product to conditions that are outside of the tolerances provided in its specification.



# **Operating Conditions**

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	
Maximum Supply Voltage	Vcc	-	-0.6	-	6.0	V	
Maximum Control Voltage	V <sub>C</sub>	-	-0.5	-	V <sub>CC</sub>	V	
			2.36	2.5	2.63		
Supply Voltage	$V_{CC}$	±5%	2.85	3.0	3.15	V	
			3.14	3.3	3.47		
Supply Current	Icc	-	-	-	3.5	mA	
Output Load	R <sub>L</sub> //C <sub>L</sub>	-	10	10k Ohm//10pF			
			-20		+70		
Operating Temperature	T <sub>A</sub>	-	-30	+25	+85	°C	
			-40		+85		
Storage Temperature	Tstg	-	-55	-	+125	°C	

# Frequency Stability

PARAMETER	SYMBOL	CONDITIONS	CONDITIONS MIN TYP MA			UNIT
Frequency Range	fo	-			MHz	
Frequency Stability						
Initial Calibration	$\Delta f/f_{O}$	Calibration @ +25°C, After 2 Reflows	-2.0	-	2.0	ppm
Temperature Only		Referenced to +25°C Reading	0.5, 1.0, 1.5, 2.0, 2.5			±ppm
Voltage Coefficient	$\Delta f/f_{25}$	Supply Voltage, ±5%	-0.2	-	0.2	
Load Coefficient		Load, ±10%	-0.2	-	0.2	ppm
Aging	A.C./C	First Year @ +25°C, nominal $V_{\text{CC}}$ and $V_{\text{C}}$	-1.0	-	1.0	
	$\Delta f/f_{25}$	10 Years @ +25°C, nominal $V_{\text{CC}}$ and $V_{\text{C}}$	-10	-	10	ppm

# **Output Parameters**

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Output Type	-	DC Coupled		Clipped Sine		-
Output Voltage Levels	Vo	-	0.8	-	-	$V_{P-P}$
Output Duty Cycle	SYM	@ 50% Level	vel 45 -		55	%
Start Up Time	Ts	-	-	-	5	ms
Phase Noise	-	See Typical Plots	-	-	-	-



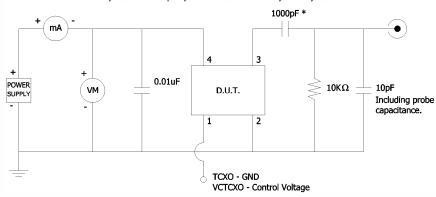
# Control Voltage

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Control Voltage	V	2.5V	0.40	1.40	2.40	\/
Control Voltage	Vc	3.0V, 3.3V	0.50	1.50	2.50	V
Frequency Tuning [VCTCXO Only]	$\Delta f/f_{O}$	Specified V <sub>C</sub> Range		±5 - ±12		
Linearity	L	Best Straight Line Fit	-	-	±10	%
Input Impedance	$Z_{Vc}$	-	- 100 -		-	kOhms
Transfer Function	-	-		Positive		-

#### **Test Circuit**

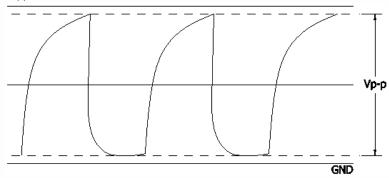
Clipped Sine

\* DC-Cut Capacitor: Add 1000pF capacitor between the TCXO output and input of load.



# **Output Waveform**

Clipped Sine

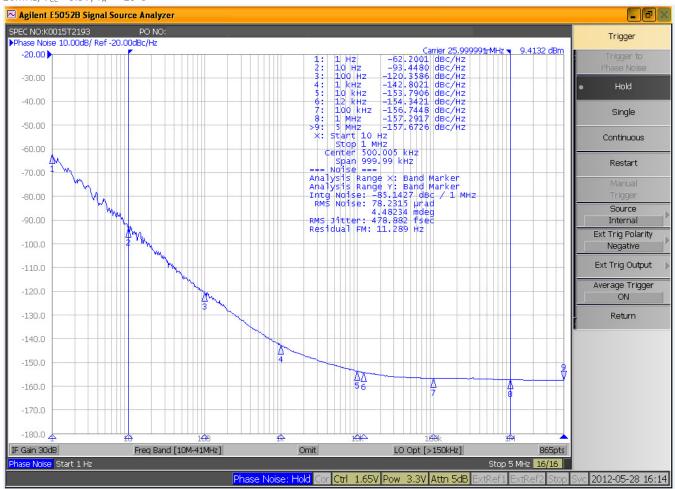




#### Performance Data

Phase Noise [typical]

26MHz,  $V_{CC} = 3.3V$ ,  $T_A = +25$ °C

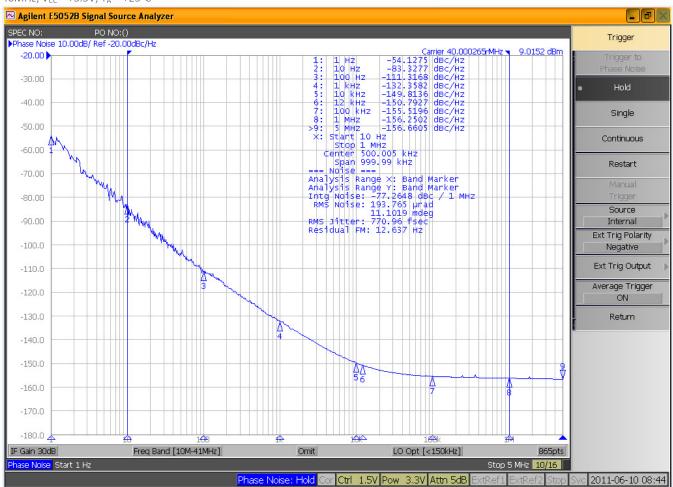




#### Performance Data

Phase Noise [typical]

40MHz,  $V_{CC} = +3.3V$ ,  $T_A = +25$ °C





# **Mechanical Specifications**

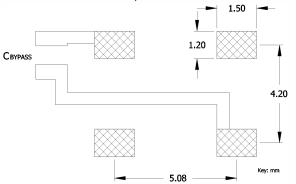
# Package Drawing 7.0 ±0.2 1.4 ±0.1 M585 CDxxx 4.00 ±0.15 5.08 ±0.2 0.2 Typ.

# Marking Information

- 1. M585 CTS Model Series.
- 2. – Pin 1 Identifier.
- 3. C-CTS
- 4. D Date Code. See Table I for codes.
- 5. xxx Frequency Code, 3-digits.

[See document 016-1454-0, Frequency Code Tables.]

#### Recommended Pad Layout



#### Notes

- DO NOT make connections to non-labeled pins or castellations as they may have internal connections used in the manufacturing process.
- 2. JEDEC termination code (e4). Barrier-plating is nickel [Ni] with gold [Au] flash plate.
- Reflow conditions per JEDEC J-STD-020; +260°C maximum, 20 seconds.
- 4. MSL = 1.

#### Pin Assignments

Symbol Function					
GND	Ground [Note 1]				
$V_{C}$	Voltage Control [VC-TCXO]				
GND	Circuit & Package				
Output	RF Output [Note 2]				
$V_{CC}$	Supply Voltage				
	GND V <sub>C</sub> GND Output				

#### Notes

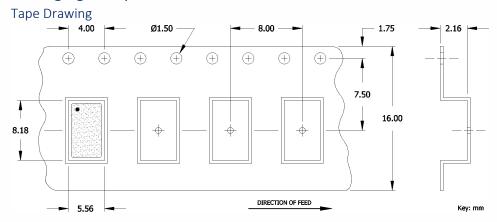
- 1. Connect to ground for TCXO, no frequency tuning.
- 2. DC-Cut capacitor required. Add 1000pF capacitor between TCXO output and input of load.

# Table I - Date Code, Beginning year 2021

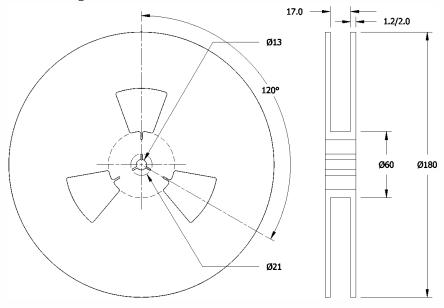
		ı	монтн		LAN	r c p	MAAD	ADD	NA AV	IIIN		ALIC	CED	OCT	NOV	DEC
	YE	AR			JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	ост	NOV	DEC
2021	2025	2029	2033	2037	А	В	С	D	Е	F	G	Н	J	K	L	М
2022	2026	2030	2034	2038	N	Р	Q	R	S	T	U	V	W	Х	Υ	Z
2023	2027	2031	2035	2039	a	b	С	d	е	f	g	h	j	k	I	m
2024	2028	2032	2036	2040	n	р	q	r	S	t	u	V	W	Х	У	Z



# Packaging - Tape and Reel



# Reel Drawing



#### Notes

- 1. Device quantity is 1k pieces per 180mm reel.
- 2. Complete CTS part number, frequency value and date code information must appear on reel and carton labels.







# Addendum

# Common Frequencies – MHz

FREQUENCY	ORDERING CODE	FREQUENCY	ORDERING CODE	FREQUENCY	ORDERING CODE	FREQUENCY	ORDERING CODE
8.000000	080	27.000000	270				
8.192000	81	30.720000	307				
10.000000	100	40.000000	400				
12.288000	122	50.000000	500				
12.800000	128						
16.000000	160						
16.384000	163						
19.200000	192						
19.440000	194						
20.000000	200						
25.000000	250						
26.000000	260						